

NDS9955

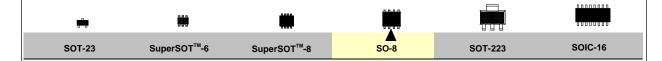
Dual N-Channel Enhancement Mode Field Effect Transistor

General Description

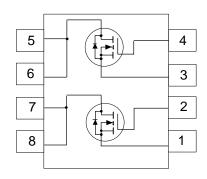
SO-8 N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, high cell density, DMOS technology. This very high density process is especially tailored to provide superior switching performance and minimize on-state resistance. These devices are particularly suited for low voltage applications such as disk drive motor control, battery powered circuits where fast switching, low in-line power loss, and resistance to transients are needed.

Features

- $\begin{tabular}{ll} \blacksquare & 3.0 \ A, 50 \ V. \ R_{\rm DS(ON)} = 0.130 \ \Omega \ @ \ V_{\rm GS} = 10 \ V, \\ R_{\rm DS(ON)} = 0.200 \ \Omega \ @ \ V_{\rm GS} = 4.5 \ V. \\ \end{tabular}$
- High density cell design for extremely low R_{DS(ON)}.
- High power and current handling capability in a widely used surface mount package.
- Dual MOSFET in surface mount package.





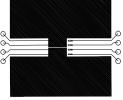


Absolute Maximum Ratings $T_A = 25^{\circ}\text{C}$ unless other wise noted

Symbol	Parameter	NDS9955	Units
V _{DSS}	Drain-Source Voltage	50	V
V_{GSS}	Gate-Source Voltage	±20	V
D	Drain Current - Continuous (Note 1a)	3	А
	- Pulsed	10	
P _D	Power Dissipation for Dual Operation	2	W
	Power Dissipation for Single Operation (Note 1a)	1.6	
	(Note 1b)	1	
	(Note 1c)	0.9	
Γ_{J} , T_{STG}	Operating and Storage Temperature Range	-55 to 150	°C
THERMA	L CHARACTERISTICS		·
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	78	°C/W
R _{eJC}	Thermal Resistance, Junction-to-Case (Note 1)	40	°C/W

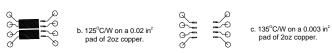
Symbol	Parameter Conditions				Тур	Max	Units
OFF CHAR	ACTERISTICS						
3V _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$		50			V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	$I_D = 250 \mu\text{A}$, Referenced t	o 25°C		60		mV/°C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = 40 \text{ V}, \ V_{GS} = 0 \text{ V}$				2	μA
GSSF	Gate - Body Leakage, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
GSSR	Gate - Body Leakage, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
ON CHARA	CTERISTICS (Note 2)	<u>. </u>					
GS(th)	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		1	1.7	3	V
			T _J =125°C	0.7		2.2	
DS(ON)	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$	•		0.076	0.13	Ω
			T _J =125°C		0.124	0.2	
		$V_{GS} = 4.5 \text{ V}, I_D = 1.5 \text{ A}$	1 -		0.103	0.2	
			T _J =125°C		0.166	0.3	
)(ON)	On-State Drain Current	$V_{GS} = 10 \text{ V}, \ V_{DS} = 10 \text{ V}$	'	10			Α
FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, I_{D} = 3 \text{ A}$			5.3		S
	CH ARACTERISTICS						
iss	Input Capacitance	$V_{DS} = 25 \text{ V}, \ V_{GS} = 0 \text{ V},$ f = 1.0 MHz			345		pF
oss	Output Capacitance	f = 1.0 MHz	f = 1.0 MHz				pF
rss	Reverse Transfer Capacitance				25		pF
WITCHING	CHARACTERISTICS (Note 2)	<u>. </u>					
D(on)	Tum - On Delay Time	$V_{DS} = 25 \text{ V}, I_{D} = 1 \text{ A}$			5	20	ns
	Turn - On Rise Time	$V_{GS} = 10 \text{ V}, R_{GFN} = 6 \Omega$			7.5	20	
D(off)	Turn - Off Delay Time				20	70	
	Turn - Off Fall Time				7	5	
Q_g	Total Gate Charge	$V_{DS} = 25 \text{ V}, I_D = 2 \text{ A},$			12.9	30	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V			1.7		
) _{ad}	Gate-Drain Charge				3.2		
RAIN-SOU	IRCE DIODE CHARACTERISTICS AND MA	XIMUM RATINGS					
;	Maximum Continuous Drain-Source Diode F	Forward Current				1.3	Α
, SD	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 1.3 \text{ A}$ (Not	e 2)		0.8	1.2	V
r	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{F} = 1.3 \text{ A},$			40		ns
rr	Reverse Recovery Current	$dI_F/dt = 100 \text{ A/}\mu\text{s}$		1.5		Α	

1. R_{BA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BAC} is guaranteed by design while R_{BCA} is determined by the user's board design.



a. 78°C/W on a 0.5 in² pad of 2oz copper.





Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

Typical Electrical Characteristics

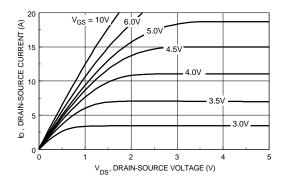


Figure 1. On-Region Characteristics.

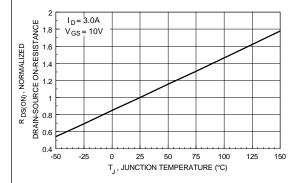


Figure 3. On-Resistance Variation With Temperature.

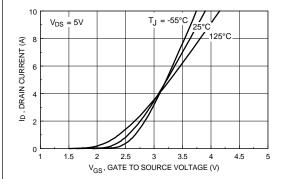


Figure 5. Transfer Characteristics.

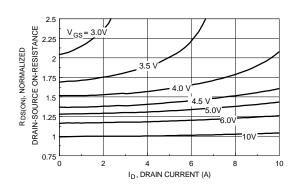


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

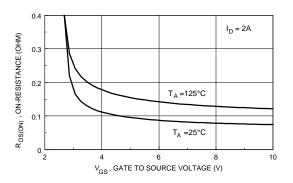


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

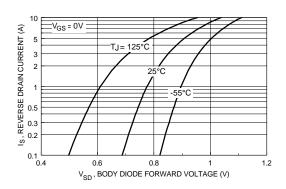
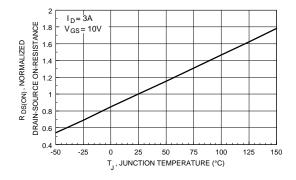


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Electrical Characteristics (continued)



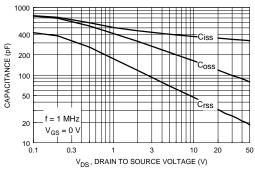


Figure 7. Gate Charge Characteristics.

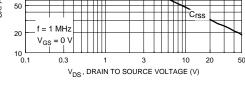
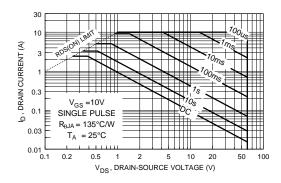


Figure 8. Capacitance Characteristics.



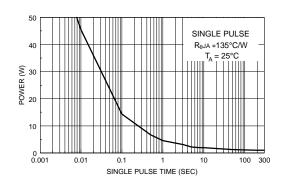


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

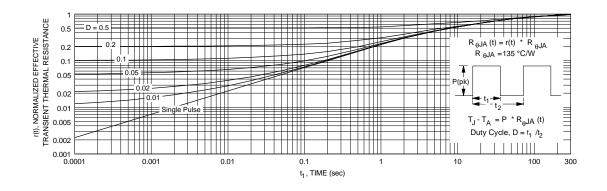


Figure 11. Transient Thermal Response Curve.

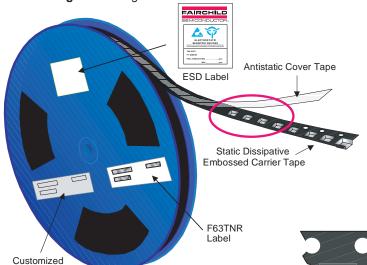
Thermal characterization performed using the conditions described in Note 1c. thermal response will change depending on the circuit board design.

Transient

SO-8 Tape and Reel Data and Package Dimensions



SOIC(8lds) Packaging Configuration: Figure 1.0



Packaging	Description

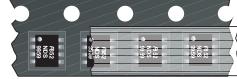
Packaging Description:

SOIC-8 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and amit-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13° or 300cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (antistatic coated). Other option comes in 500 units per 7° or 177cm diameter reel. This and some other options are further described in the Packaging Information table.

These full reles are individually barcode labeled and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains two reels maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped.

ESD Label

F63TN Label





SOIC-8 Unit Orientation

343mm x 342mm x 64mm Standard Intermediate box

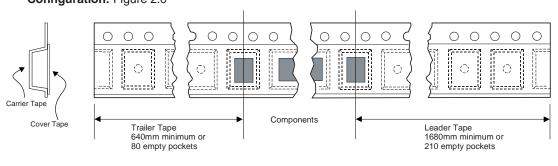
SOIC (8lds) Packaging Information Packaging Option Standard o flow code) L86Z D84Z Rail/Tube TNR Packaging type TNR TNR Qty per Reel/Tube/Bag 2.500 4.000 500 Reel Size 13" Dia 13" Dia 7" Dia Box Dimension (mm) 343y64y343 530x130x83 343y64y343 184v187v47 Max qty per Box 5,000 30,000 8,000 1,000 Weight per unit (gm) 0.0774 0.0774 0.0774 0.0774 Weight per Reel (kg) 0.6060 0.9696 0.1182 Note/Comments

F63TNR Label sample

Label



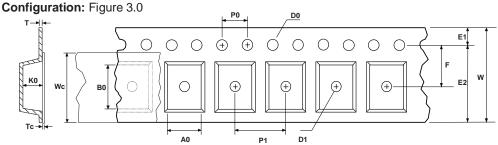
SOIC(8lds) Tape Leader and Trailer Configuration: Figure 2.0



F63TNL



SOIC(8lds) Embossed Carrier Tape





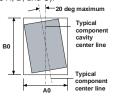
Dimensions are in millimeter														
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SOIC(8lds) (12mm)	6.50 +/-0.10	5.30 +/-0.10	12.0 +/-0.3	1.55 +/-0.05	1.60 +/-0.10	1.75 +/-0.10	10.25 min	5.50 +/-0.05	8.0 +/-0.1	4.0 +/-0.1	2.1 +/-0.10	0.450 +/- 0.150	9.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



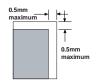
Sketch A (Side or Front Sectional View)
Component Rotation

13" Diameter Option



Sketch B (Top View)

Component Rotation

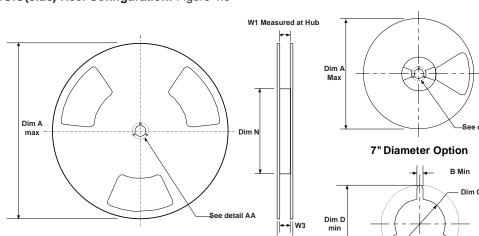


Sketch C (Top View)

Component lateral movement

DETAIL AA

SOIC(8lds) Reel Configuration: Figure 4.0

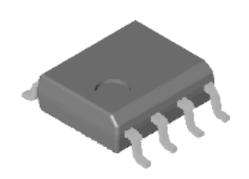


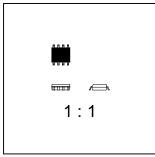
	Dimensions are in inches and millimeters								
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
12mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4
12mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	7.00 178	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4

W2 max Measured at Hub

SO-8 Tape and Reel Data and Package Dimensions, continued

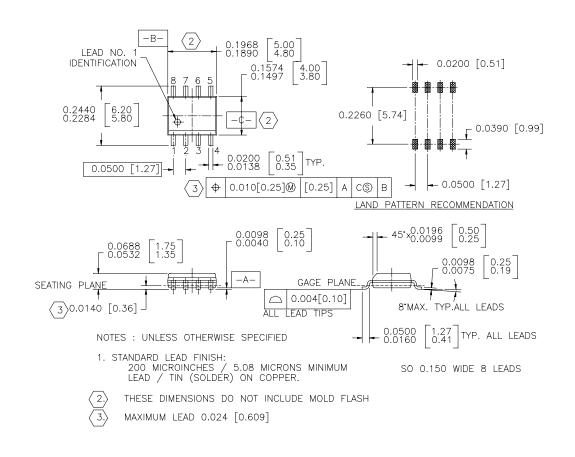
SOIC-8 (FS PKG Code S1)





Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0774



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FACT™ QFET™ FACT Quiet Series™ QS™

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