

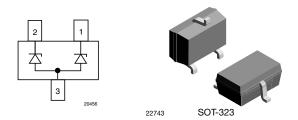
Vishay Semiconductors

HALOGEN

FREE GREEN

(5-2008)

Dual-Line ESD-Protection Diode Array in SOT-323

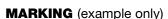


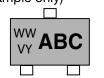
FEATURES

- Compact SOT-323 package
- 2-line unidirectional ESD-protection
- AEC-Q101 qualified available
- Working range 1 V to 33 V
- ESD immunity acc. IEC 61000-4-2 ±15 kV to ±30 kV contact discharge ±15 kV to ±30 kV air discharge



- soldering can be checked by standard vision inspection
- AOI = Automated Optical Inspection
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ABC = type code (see table below) WW = date code working week VY = date code year

LINKS TO ADDITIONAL RESOURCES



ORDERING INFORMATION					
		ENVIRONM	IENTAL AND QUAL	ITY CODE	
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	TIN PLATED	3K PER 7" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)
		GREEN		15K = MOQ	
VESD05A2-03G	-	G	3	-08	VESD05A2-03G-G3-08
VESD05A2-03G	Н	G	3	-08	VESD05A2-03GHG3-08

PACKAGE DATA																				
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS														
VESD01A2-03G-G3	SOT-323	D01																		
VESD03A2-03G-G3	SOT-323	D03	ı	 -																
VESD05A2-03G-G3	SOT-323	D05		MSL level 1 Pack tame																
VESD08A2-03G-G3	SOT-323	D08	F 0 ma		MSL level 1	Dools town over the many 000 °C														
VESD12A2-03G-G3	SOT-323	D12	5.2 mg	5.2 mg	5.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	3.2 mg	5.2 mg	UL 94 V-0 (according J-STD-020)	5.2 mg UL 94 V-0	(according J-STD-020)	Peak temperature max. 260 °C
VESD16A2-03G-G3	SOT-323	D16																		
VESD26A2-03G-G3	SOT-323	D26																		
VESD33A2-03G-G3	SOT-323	D33																		

VESD01A2-03G to VESD33A2-03G

ABSOLUTE MAXIMUM RATINGS VESD01A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	11	Α		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	70	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV		
E3D IIIIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T _{stg}	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD03A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	11.6	Α	
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W	
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV	
Operating temperature	Junction temperature	TJ	-55 to +150	°C	
Storage temperature		T _{stg}	-55 to +150	°C	

ABSOLUTE MAXIMUM RATINGS VESD05A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	8.7	Α	
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W	
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	\/	30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV	
Operating temperature	Junction temperature	T _J	-55 to +150	°C	
Storage temperature		T _{stg}	-55 to +150	°C	

ABSOLUTE MAXIMUM RATINGS VESD08A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	6.60	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W		
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T _{stg}	-55 to +150	°C		

VESD01A2-03G to VESD33A2-03G

ABSOLUTE MAXIMUM RATINGS VESD12A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	4.4	Α		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV		
E3D IIIIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV		
Operating temperature	Junction temperature	T _J	-55 to +150	°C		
Storage temperature		T _{stg}	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD16A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	3.6	А	
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	30	kV	
E3D IIIIIIuriity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	30	kV	
Operating temperature	Junction temperature	T _J	-55 to +150	°C	
Storage temperature		T _{stg}	-55 to +150	°C	

ABSOLUTE MAXIMUM RATINGS VESD26A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	2.1	Α	
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P _{PP}	100	W	
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	20	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	20	kV	
Operating temperature	Junction temperature	TJ	-55 to +150	°C	
Storage temperature		T _{stg}	-55 to +150	°C	

ABSOLUTE MAXIMUM RATINGS VESD33A2-03G T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	1.6	Α	
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	P_{PP}	100	W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	15	kV	
E3D Infinitrity	Air discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	15	kV	
Operating temperature	Junction temperature	T _J	-55 to +150	°C	
Storage temperature		T _{stg}	-55 to +150	°C	



ELECTRICAL CHARACTERISTICS VESD01A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	1	V
Reverse voltage	at I _R = 100 μA	V_R	1	1.2	-	V
Reverse current	at V _R = 1 V	I _R	-	20	100	μΑ
Reverse breakdown voltage	at I _R = 20 mA	V_{BR}	2.5	2.65	2.8	V
Reverse clamping voltage	at I _{PP} = I _{PPM} = 11 A, t _p = 8/20 μs	V _C	-	5.6	6.4	V
Converd elemning veltage	at I _{PP} = 1 A, t _p = 300 μs	V _F	0.9	1.1	1.2	V
Forward clamping voltage	at I _{PP} = I _{PPM} = 11 A, t _p = 8/20 μs	V_{F}	-	2.5	3.2	V
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.13	-	Ω
Capacitance	at V _R = 0 V; f = 1 MHz	C_D	153	192	230	pF

ELECTRICAL CHARACTERISTICS VESD03A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	3	V
Reverse voltage	at I _R = 20 μA	V_R	3	-	-	V
Reverse current	at V _R = 3 V	I _R	-	8	20	μA
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	4.4	4.65	4.9	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 11.6 \text{ A}, t_p = 8/20 \mu \text{s}$	V _C	-	7.8	8.70	V
Converd elemping veltage	at $I_{PP} = 1 \text{ A}, t_p = 300 \mu s$	V_{F}	0.9	1.1	1.2	V
Forward clamping voltage	at I _{PP} = I _{PPM} = 11.6 A, t _p = 8/20 μs	V _F	-	2.6	3.32	V
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.19	-	Ω
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	89	112	135	pF

ELECTRICAL CHARACTERISTICS VESD05A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	5	V	
Reverse voltage	at I _R = 1 μA	V_R	5	-	-	V	
Reverse current	at V _R = 5 V	I _R	-	0.01	0.1	μΑ	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	6.85	7.26	7.65	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}, t_p = 8/20 \mu\text{s}$	V _C	-	10.3	11.5	V	
Famuard alamaina valtaga	at I _{PP} = 1 A, t _p = 300 μs	V _F	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 8.7 \text{ A}, t_p = 8/20 \mu\text{s}$	V_{F}	-	2.2	2.74	V	
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.2	-	Ω	
Capacitance	at $V_R = 0 V$; $f = 1 MHz$	C _D	53	67	81	pF	



ELECTRICAL CHARACTERISTICS VESD08A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	8	V
Reverse voltage	at I _R = 0.1 μA	V_R	8	-	-	V
Reverse current	at V _R = 8 V	I _R	-	0.01	0.1	μΑ
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	9.5	10	10.5	V
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 6.6 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V _C	-	13.7	15.3	V
Faruard alamaina valtaga	at I _{PP} = 1 A, t _p = 300 μs	V_{F}	0.9	1.1	1.2	V
Forward clamping voltage	at I _{PP} = I _{PPM} = 6.6 A, t _p = 8/20 μs	V_{F}	-	1.9	2.32	V
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.23	-	Ω
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	37	47	57	pF

ELECTRICAL CHARACTERISTICS VESD12A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	12	V	
Reverse voltage	at I _R = 0.1 μA	V_R	12	-	-	V	
Reverse current	at V _R = 12 V	I _R	-	0.01	0.1	μA	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	13.9	14.7	15.5	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 4.4 \text{ A}, t_p = 8/20 \mu s$	V _C	-	20.5	22.7	V	
Famusard alamaina valtaga	at $I_{PP} = 1 \text{ A}$, $t_p = 300 \mu\text{s}$	V_{F}	0.9	1.1	1.2	V	
Forward clamping voltage	at I _{PP} = I _{PPM} = 4.4 A, t _p = 8/20 μs	V _F	-	1.6	1.88	V	
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.4	-	Ω	
Capacitance	at $V_R = 0 V$; $f = 1 MHz$	C _D	26	33	40	pF	

ELECTRICAL CHARACTERISTICS VESD16A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	16	V	
Reverse voltage	at I _R = 0.1 μA	V_R	16	-	-	V	
Reverse current	at V _R = 16 V	I _R	-	0.01	0.1	μΑ	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	17	17.9	18.8	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 3.6 \text{ A}, t_p = 8/20 \mu\text{s}$	V _C	-	25.3	28	V	
Famous de la serie a contra de	at I _{PP} = 1 A, t _p = 300 μs	V _F	0.9	1.1	1.2	V	
Forward clamping voltage	at I _{PP} = I _{PPM} = 3.6 A, t _p = 8/20 μs	V _F	-	1.5	1.72	V	
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	0.53	-	Ω	
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	21	27	33	pF	



ELECTRICAL CHARACTERISTICS VESD26A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	26	V	
Reverse voltage	at I _R = 0.1 μA	V_R	26	-	-	V	
Reverse current	at V _R = 26 V	I _R	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	27.6	29.1	30.6	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 2.1 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V _C	-	43	48	V	
Forward clamping voltage	at $I_{PP} = 1 \text{ A}$, $t_p = 300 \mu\text{s}$	V_{F}	0.9	1.1	1.2	V	
Forward clamping voltage	at $I_{PP} = I_{PPM} = 2.1 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V _F	-	1.3	1.42	V	
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	1.9	-	Ω	
Capacitance	at V _R = 0 V; f = 1 MHz	C _D	14	17.5	21	pF	

ELECTRICAL CHARACTERISTICS VESD33A2-03G (T _{amb} = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines	
Reverse stand off voltage	Max. reverse working voltage	V_{RWM}	-	-	33	V	
Reverse voltage	at I _R = 0.1 μA	V_R	33	-	-	V	
Reverse current	at V _R = 33 V	I _R	-	< 0.01	0.1	μΑ	
Reverse breakdown voltage	at I _R = 1 mA	V_{BR}	35.5	37.4	39.3	V	
Reverse clamping voltage	at $I_{PP} = I_{PPM} = 1.6 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V _C	-	56	62.5	V	
Converd elemning veltage	at $I_{PP} = 1 \text{ A}$, $t_p = 300 \mu\text{s}$	V_{F}	0.9	1.1	1.2	V	
Forward clamping voltage	at I _{PP} = I _{PPM} = 1.6 A, t _p = 8/20 μs	V _F	-	1.22	1.32	V	
Dynamic resistance	t _p = 100 ns (TLP; reverse direction)	r _{dyn}	-	3.6	-	Ω	
Capacitance	at V _R = 0 V; f = 1 MHz	C_{D}	12	15	18	pF	

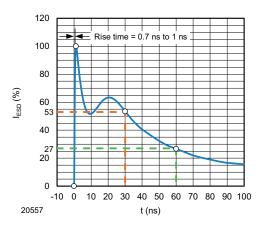


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

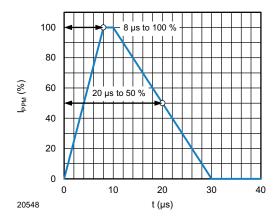


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

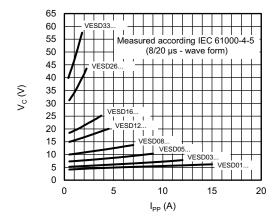


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

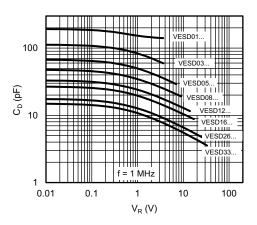


Fig. 4 - Typical Capacitance vs. Reverse Voltage

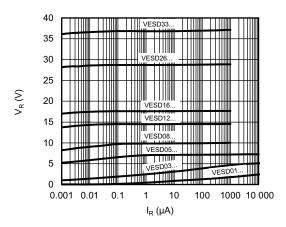


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

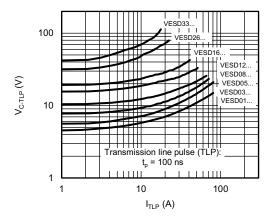


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

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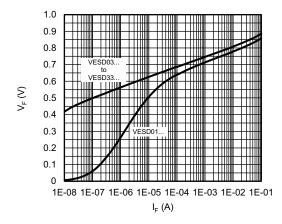


Fig. 7 - Typical Forward Voltage vs. Forward Current

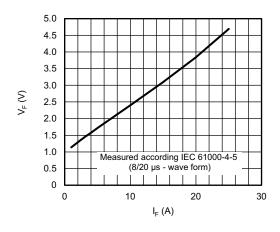
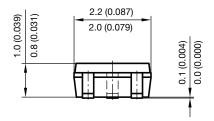
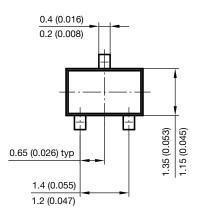


Fig. 8 - Typical Forward Voltage vs. Forward Current

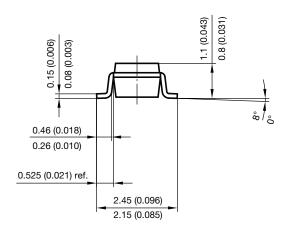
PACKAGE DIMENSIONS in millimeters (inches): SOT-323



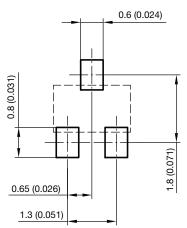


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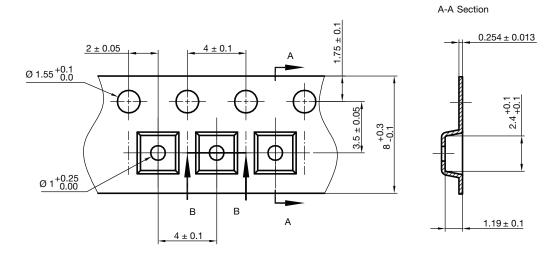
foot print recommendation:



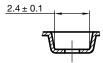


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CARRIER TAPE SOT-323

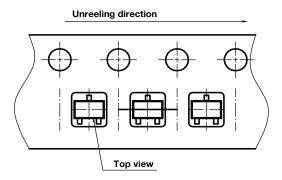


B-B Section



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ORIENTATION IN CARRIER TAPE SOT-323



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