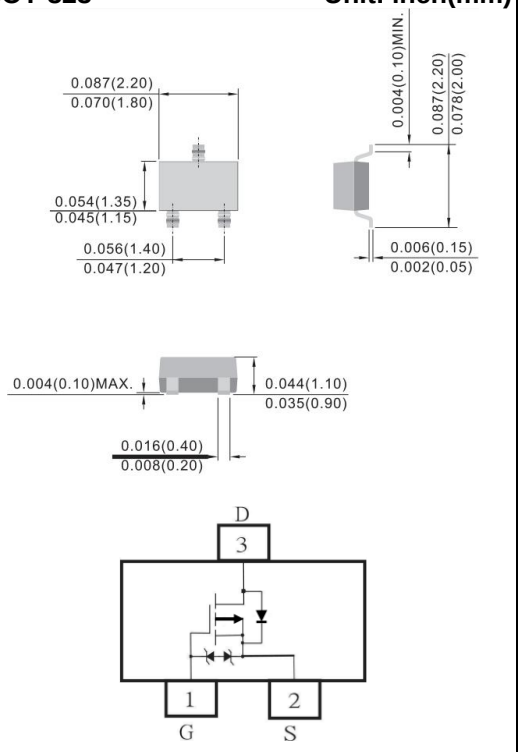
PJC7403

20V P-Channel Enhancement Mode MOSFET – ESD Protected

Voltage	-20 V	Current	-0.7A

SOT-323 Unit: inch(mm)



Features

- RDS(ON) , VGS@-4.5V, ID@-0.7A<325mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.6A<420mΩ
- RDS(ON) , VGS@-1.8V, ID@-0.5A<600mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-323 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0002 ounces, 0.005 grams
- Marking : C03

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current		I_D	-0.7	A
Pulsed Drain Current (Note 4)		I_{DM}	-2.8	A
Power Dissipation	T_a=25°C	P_D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	357	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250uA	-0.5	-0.64	-1.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=-4.5V, I_D=-0.7A	-	260	325	mΩ
		V_{GS}=-2.5V, I_D=-0.6A	-	310	420	
		V_{GS}=-1.8V, I_D=-0.5A	-	400	600	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-20V, V_{GS}=0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V	-	±3.5	±10	uA
Dynamic						
Total Gate Charge	Q_g	V_{DS}=-10V, I_D=-0.7A, V_{GS}=-4.5V (Note 1,2)	-	2.2	-	nC
Gate-Source Charge	Q_{gs}		-	0.4	-	
Gate-Drain Charge	Q_{gd}		-	0.5	-	
Input Capacitance	Ciss	V_{DS}=-10V, V_{GS}=0V, f=1.0MHZ	-	165	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss		-	14.7	-	
Switching						
Turn-On Delay Time	$td_{(on)}$	V_{DD}=-10V, I_D=-0.7A, V_{GS}=-4.5V, R_G=6Ω (Note 1,2)	-	8.9	-	ns
Turn-On Rise Time	tr		-	37	-	
Turn-Off Delay Time	$td_{(off)}$		-	127	-	
Turn-Off Fall Time	tf		-	70	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-1	A
Diode Forward Voltage	V_{SD}	I_S=-1A, V_{GS}=0V	-	-0.97	-1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.

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TYPICAL CHARACTERISTIC CURVES

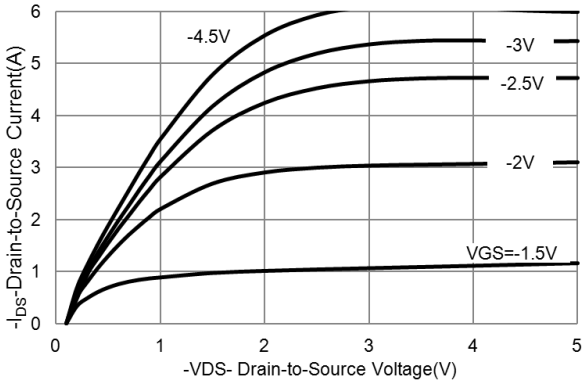

Fig.1 On-Region Characteristics

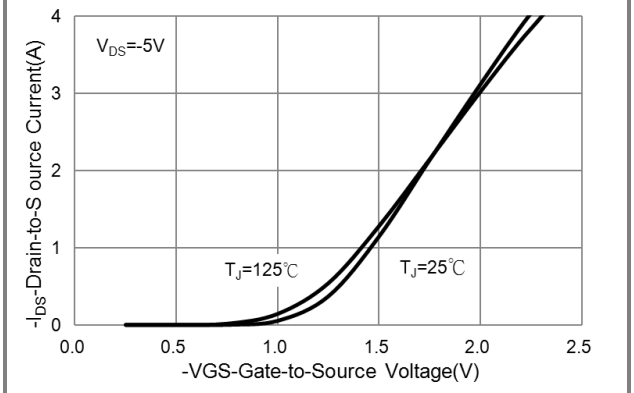

Fig.2 Transfer Characteristics

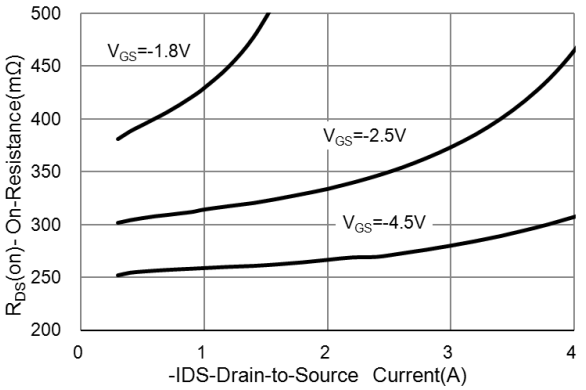

Fig.3 On-Resistance vs. Drain Current

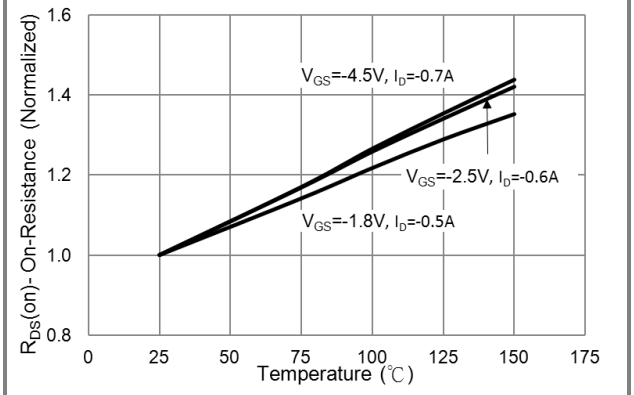

Fig.4 On-Resistance vs. Junction temperature

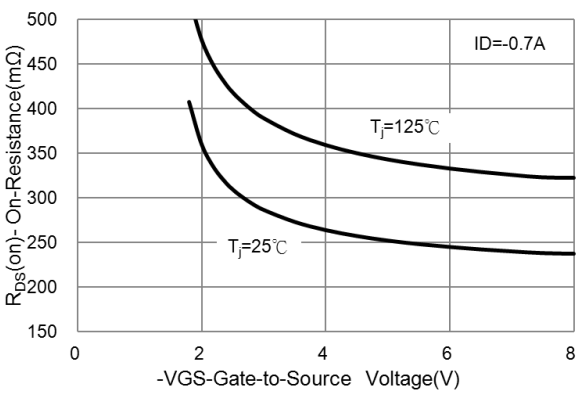

Fig.5 On-Resistance Variation with VGS.

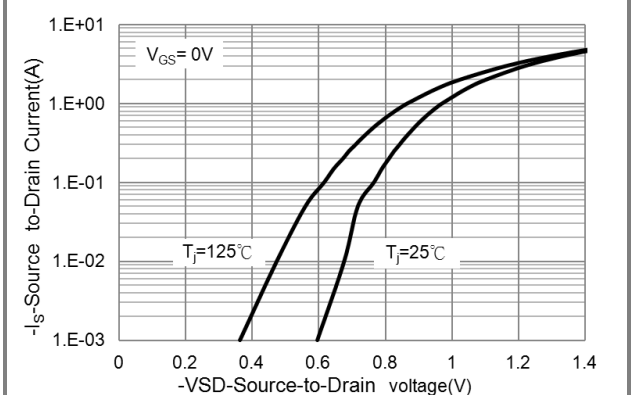

Fig.6 Body Dlode CharacterIslcs

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TYPICAL CHARACTERISTIC CURVES

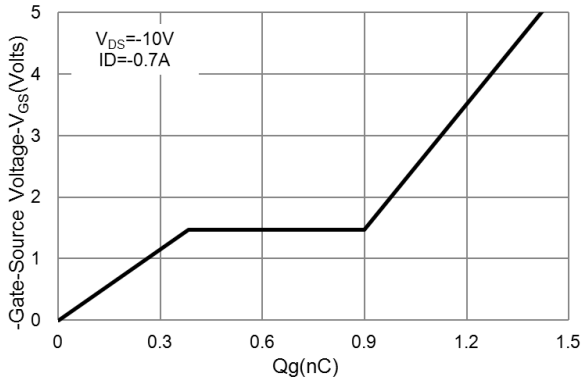


Fig.7 Gate-Charge Characteristics

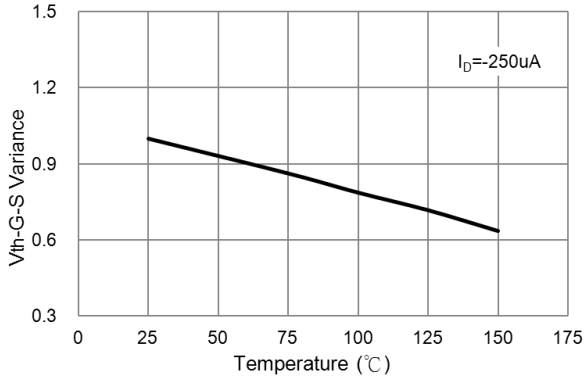


Fig.8 Threshold Voltage Variation with Temperature.

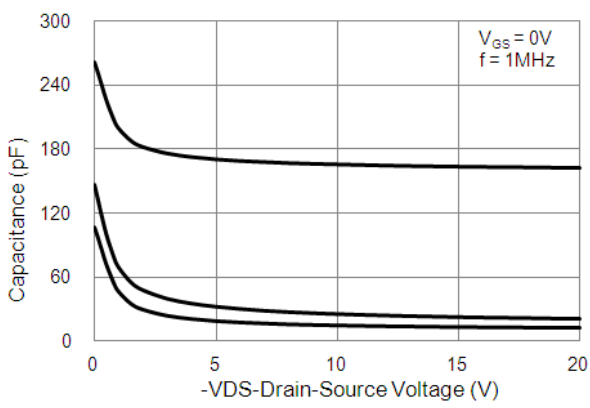


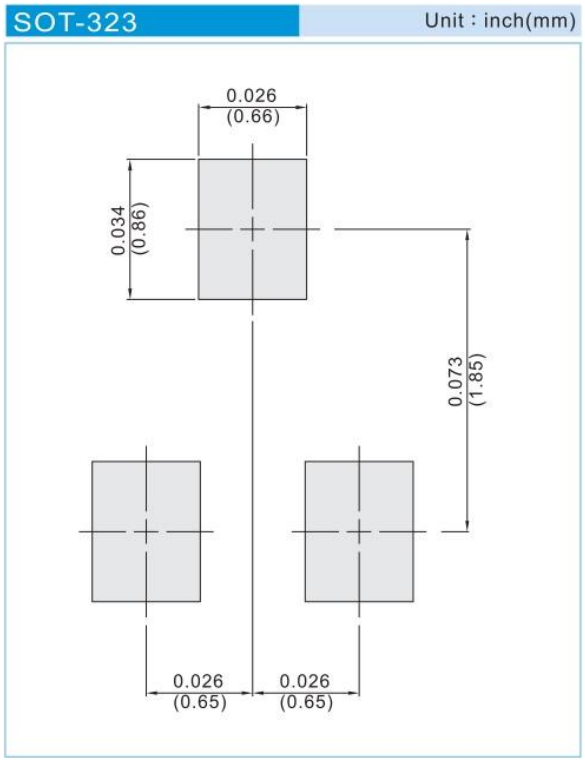
Fig.9 Threshold Voltage Variation with Temperature.

PJC7403

PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJC7403_R1_00001	SOT-323	3K pcs / 7” reel	C03	Halogen free
PJC7403_R2_00001	SOT-323	12K pcs / 13” reel	C03	Halogen free

ORDER INFORMATION



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