MOSFET – P-Channel, QFET®

-60 V, -17 A, 70 m Ω

FQPF27P06

Description

This P-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

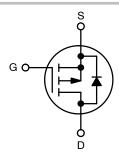
- -17 A, -60 V, $R_{DS(on)} = 70 \text{ m}\Omega$ (Max.) @ $V_{GS} = -10 \text{ V}$, $I_D = -8.5 \text{ A}$
- Low Gate Charge (Typ. 33 nC)
- Low Crss (Typ. 120 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating



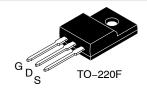
ON Semiconductor®

www.onsemi.com

V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
-60 V	70 mΩ @ 10 V	–17 A	

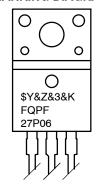


P-Channel MOSFET



TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = 3-Digit Plant Code

&K = 2-Digits Lot Run Traceability Code

FQPF27P06 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
FQPF27P06	TO-220-3 (Pb-Free)	1000 Units / Tube

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise specified)

Symbol	Paramet	FQPF27P06	Unit	
V_{DSS}	Drain-Source Voltage		-60	V
Ι _D	Drain Current – Continuous (T _C = 25°C)		-17	Α
		- Continuous (T _C = 100°C)	-12	Α
I _{DM}	Drain Current (Note 1)	- Pulsed	-68	Α
V _{GSS}	Gate-Source Voltage		+ 25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		560	mJ
I _{AR}	Avalanche Current (Note 1)		-17	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		4.7	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		-7.0	V/ns
P_{D}	Power Dissipation (T _C = 25°C)		47	W
		– Derate above 25°C	0.31	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering Purposes, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. L = 2.25 mH, $I_{AS} = -17$ A, $V_{DD} = -25$ V, $R_G = 25$ Ω , Starting $T_J = 25^{\circ}C$ 3. $I_{SD} \le -27$ A, di/dt ≤ 300 A/ μ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL CHARACTERISTICS

Symbol	Characteristic		Max	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction-to-Case	_	3.19	°C/W
$R_{ heta JA}$	R _{θJA} Thermal Resistance, Junction-to-Ambient		62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS			•		•
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$	-60		-	V
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C	-	-0.06	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -60 V, V _{GS} = 0 V	-	-	-1	μΑ
		V _{DS} = -48 V, T _C = 150°C	-	-	-10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = -25 V, V _{DS} = 0 V	-	-	-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 25 V, V _{DS} = 0 V	ı	-	100	nA
ON CHARA	ACTERISTICS					
V _{GS(th})	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-2.0	-	-4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, I_D = -8.5 \text{ A}$	-	0.055	0.07	Ω
9FS	Forward Transconductance	$V_{DS} = -30 \text{ V}, I_D = -8.5 \text{ A (Note 4)}$	-	12	-	S
DYNAMIC	CHARACTERISTICS					
C _{iss}	Input Capacitance	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$	-	1100	1400	pF
C _{oss}	Output Capacitance	7	-	510	660	pF
C _{rss}	Reverse Transfer Capacitance	7	-	120	155	pF
SWITCHIN	G CHARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -30 \text{ V}, I_D = -13.5 \text{ A}, R_G = 25 \Omega$	-	18	45	ns
t _r	Turn-On Rise Time	(Note 4, 5)	-	185	380	ns
t _{d(off)}	Turn-Off Delay Time	7	-	30	70	ns
t _f	Turn-Off Fall Time	7	1	90	190	ns
Qg	Total Gate Charge	$V_{DS} = -48 \text{ V}, I_D = -27 \text{ A}, V_{GS} = -10 \text{ V}$	1	33	43	nC
Q _{gs}	Gate-Source Charge	(Note 4, 5)	-	6.8	-	nC
Q _{gd}	Gate-Drain Charge	7	1	18	-	nC
DRAIN-SC	OURCE DIODE CHARACTERISTICS AND MAX	IMUM RATING				
Is	Maximum Continuous Drain-Source Diode Forward Current		-	-	-17	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		_	-	-68	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = -17 A	-	-	-4.0	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = -27 A,	-	105	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F / dt = 100 A/μs (Note 4)	_	0.41	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: Pulse width ≤ 300 μs, Duty cycle ≤ 2%

5. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

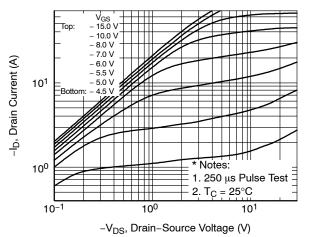


Figure 1. On-Region Characteristics

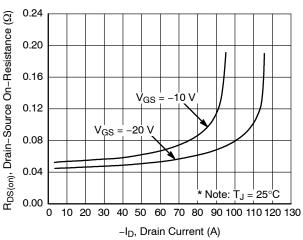


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

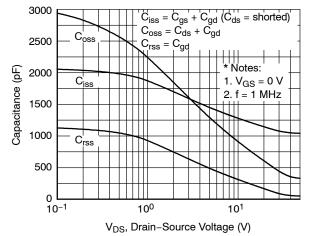


Figure 5. Capacitance Characteristics

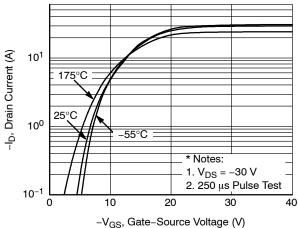


Figure 2. Transfer Characteristics

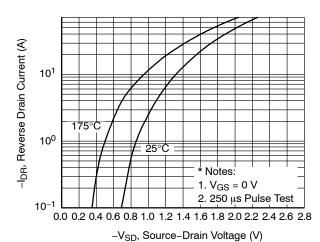


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

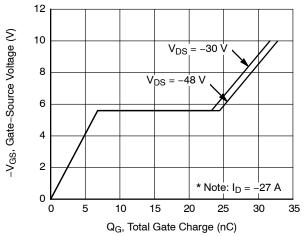


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS (Continued)

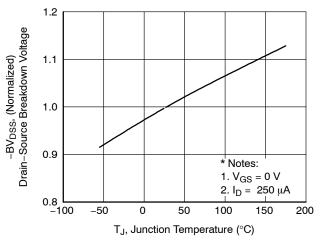


Figure 7. Breakdown Voltage Variation vs. Temperature

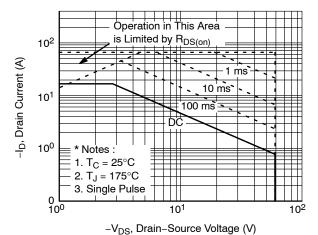
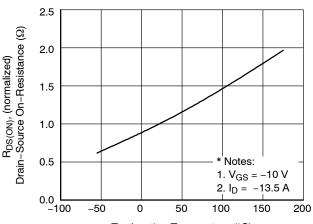


Figure 9. Maximum Safe Operating Area



T_J, Junction Temperature (°C)

Figure 8. On–Resistance Variation vs.
Temperature

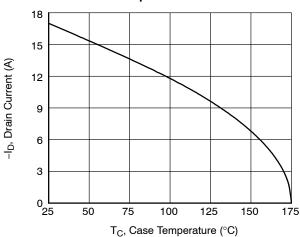


Figure 10. Maximum Drain Current vs.

Case Temperature

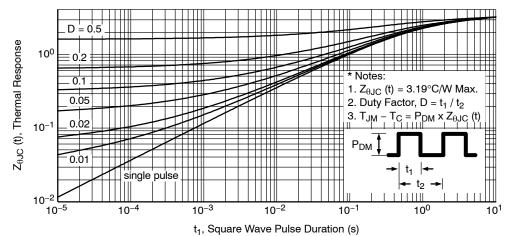


Figure 11. Transient Thermal Response Curve

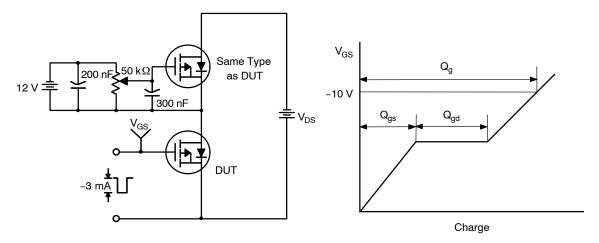


Figure 12. Gate Charge Test Circuit & Waveform

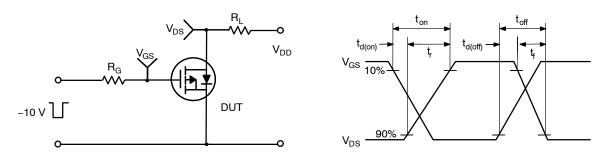


Figure 13. Resistive Switching Test Circuit & Waveforms

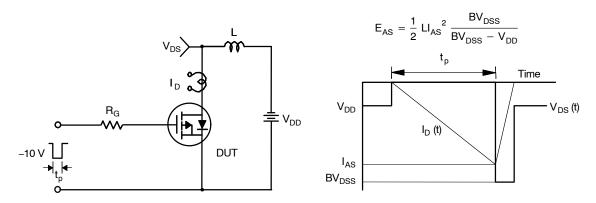
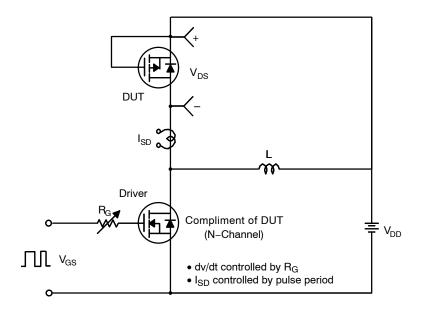


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



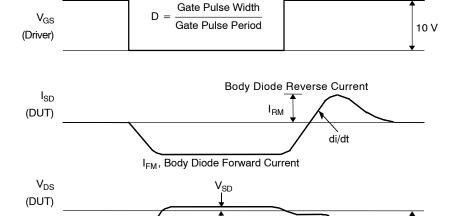


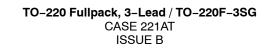
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Body Diode Recoverydv/dt

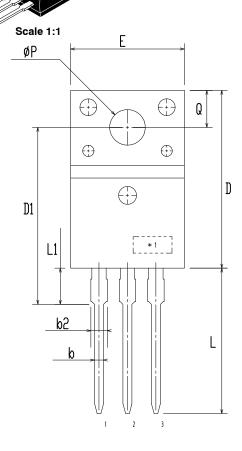
Body Diode

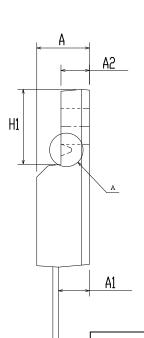
Forward Voltage Drop

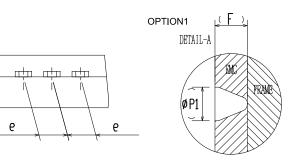
 V_{DD}



DATE 19 JAN 2021







DIM	MILLIMITERS			
ויונע	MIN	NDM	MAX	
Α	4.50	4.70	4.90	
A1	2.56	2.76	2.96	
A2	2.34	2.54	2.74	
b	0.70	0.80	0.90	
b2	~	~	1.47	
С	0.45	0.50	0.60	
D	15.67	15.87	16.07	
D1	15.60	15.80	16.00	
E	9.96	10.16	10.36	
е	2.34	2.54	2.74	
F	~	0.84	~	
H1	6.48	6.68	6.88	
L	12.78	12.98	13.18	
L1	3.03	3.23	3.43	
ØΡ	2.98	3.18	3.38	
Ø P1	~	1.00	~	
Q	3.20	3.30	3.40	

MILL IMITEDS

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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