

SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS, Galileo, and Compass

Gailleo, and Compas Rev. 5 — 21 May 2021

Product data sheet

1 **Product profile**

1.1 General description

The BGU8019 is, also known as the GPS1202M, a Low Noise Amplifier (LNA) for GNSS receiver applications, available in a small plastic 6-pin extremely thin leadless package. The BGU8019 requires one external matching inductor.

The BGU8019 adapts itself to the changing environment resulting from co-habitation of different radio systems in modern cellular handsets. It has been designed for low power consumption and optimal performance when jamming signals from co-existing cellular transmitters are present. At low jamming power levels it delivers 18.5 dB gain at a noise figure of 0.55 dB. During high jamming power levels, resulting for example from a cellular transmit burst, it temporarily increases its bias current to improve sensitivity.

1.2 Features and benefits

- Cover full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure (NF) = 0.55 dB
- Gain = 18.5 dB
- High input 1 dB compression point of -7 dBm
- High out of band IP3_i of 6 dBm
- Supply voltage 1.5 V to 3.1 V
- Self-shielding package concept
- Integrated supply decoupling capacitor
- Optimized performance at a supply current of 4.6 mA
- Power-down mode current consumption < 1 µA
- · Integrated temperature stabilized bias for easy design
- · Require only one input matching inductor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated matching for the output
- Available in 6-pins leadless package 1.1 mm × 0.7 mm × 0.37 mm; 0.4 mm pitch: SOT1232
- 180 GHz transit frequency SiGe:C technology
- Moisture sensitivity level of 1



1.3 Applications

LNA for GPS, GLONASS, Galileo, and Compass (BeiDou) in:

- smart phones
- · feature phones,
- tablet PCs
- · digital still cameras
- digital video cameras
- RF front-end modules
- complete GNSS modules
- personal health applications

1.4 Quick reference data

Table 1. Quick reference data

 $f = 1575 \text{ MHz}; V_{CC} = 2.85 \text{ V}; V_{I(ENABLE)} \ge 0.8 \text{ V}; P_i < -40 \text{ dBm}; T_{amb} = 25 \text{ °C}; input matched to 50 \Omega using a 6.8 nH inductor, see Figure 1; unless otherwise specified.$

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CC}	supply voltage			1.5	-	3.1	V
I _{CC}	supply current			-	4.6	6.6	mA
G _p	power gain	no jammer		16.5	18.5	20.5	dB
NF	noise figure	P _i = -40 dBm, no jammer	[1]	-	0.55	1.1	dB
P _{i(1dB)}	input power at 1 dB gain compression			-11	-7	-	dBm
IP3 _i	input third-order intercept point		[2]	0	6	-	dBm
			[3]	0	6	-	dBm

[1] PCB losses are subtracted.

[2] $f_1 = 1713 \text{ MHz}; f_2 = 1851 \text{ MHz}; P_i = -20 \text{ dBm per carrier}$

[3] $f_1 = 1713$ MHz, $P_i = -20$ dBm; $f_2 = 1850$ MHz, $P_i = -65$ dBm

Pinning information 2

Pin	Description	Simplified outline	Graphic symbol
1	GND		
2	V _{CC}	4 3	
3	RF_OUT		5-3
4	GND_RF	5 2	
5	RF_IN		aaa-006408
6	ENABLE	6 1	
		Transparent top view	

Ordering information 3

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BGU8019	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body $1.1 \times 0.7 \times 0.37$ mm	SOT1232			
OM7848	EVB	BGU8019 evaluation board, MMIC only	-			
OM7849	EVB	BGU8019 evaluation board, front-end EVB	-			

Marking code 4

Table 4. Marking code						
Type number	Marking code	Date code				
BGU8019	A	YWW				



Limiting values 5

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Absolute Maximum Ratings are given as Limiting Values of stress conditions during operation, that must not be exceeded under the worst probable conditions.

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CC}	supply voltage	RF input AC coupled	[1]	-0.5	5	V
V _{I(ENABLE)}	input voltage on pin ENABLE	$V_{I(ENABLE)} < V_{CC} + 0.6 V$	[1][2]	-0.5	5	V
V _{I(RF_IN)}	input voltage on pin RF_IN	DC, V _{I(RF_IN)} < V _{CC} + 0.6 V	[1][2][3]	-0.5	5	V
V _{I(RF_OUT)}	input voltage on pin RF_OUT	DC, $V_{I(RF_{OUT})} < V_{CC} + 0.6 V$	[1][2][3]	-0.5	5	V
Pi	input power		[1]	-	10	dBm
P _{tot}	total power dissipation	T _{sp} ≤ 130 °C		-	55	mW
T _{stg}	storage temperature			-65	150	°C
Tj	junction temperature			-	150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to ANSI/ESDA/JEDEC standard JS-001		-	±2	kV
		Charged Device Model (CDM) According to JEDEC standard JESD22-C101C		-	±1	kV

[1]

Stressed with pulses of 200 ms in duration, with application circuit as in Figure 1. Warning: due to internal ESD diode protection, the applied DC voltage shall not exceed V_{CC} + 0.6 V and shall not exceed 5.0 V in order to avoid excess [2] current.

[3] The RF input and RF output are AC coupled through internal DC blocking capacitors.

Recommended operating conditions 6

Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.5	-	3.1	V
T _{amb}	ambient temperature		-40	25	85	°C
V _{I(ENABLE)}	input voltage on pin ENABLE	OFF state	-	-	0.3	V
		ON state	0.8	-	-	V

Thermal characteristics 7

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		225	K/W

BGU8019 Product data sheet

Characteristics 8

Table 8. Characteristics at V_{CC} = 1.8 V

 $f = 1575 \text{ MHz}; \text{ V}_{CC} = 1.8 \text{ V}; \text{ V}_{I(ENABLE)} \ge 0.8 \text{ V}; P_i < -40 \text{ dBm}; T_{amb} = 25 \text{ °C}; input matched to 50 \Omega using a 6.8 nH inductor, the second seco$ see Figure 1; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
I _{CC}	supply current	V _{I(ENABLE)} ≥ 0.8 V						
		P _i < -40 dBm		-	4.4	6.4	mA	
		P _i = -20 dBm		-	9	-	mA	
		V _{I(ENABLE)} ≤ 0.3 V		-	-	1	μA	
G _p	power gain	no jammer		16	18	20	dB	
		P _{jam} = -20 dBm; f _{jam} = 850 MHz		-	20	-	dB	
		P _{jam} = -20 dBm; f _{jam} = 1850 MHz		-	20	-	dB	
RL _{in} in	input return loss	P _i < -40 dBm		9	12	-	dB	
		P _i = -20 dBm		-	20	-	dB	
RL _{out}	output return loss	P _i < -40 dBm		8	13	-	dB	
		P _i = -20 dBm		-	12	-	dB	
ISL	isolation			27	30	-	dB	
NF	noise figure	P _i = -40 dBm, no jammer	[1]	-	0.55	1.1	dB	
		P _i = -40 dBm, no jammer	[2]	-	0.60	1.15	dB	
		P _{jam} = -20 dBm; f _{jam} = 850 MHz	[2]	-	0.9	-	dB	
		P _{jam} = -20 dBm; f _{jam} = 1850 MHz	[2]	-	1.3	-	dB	
P _{i(1dB)}	input power at 1 dB gain compression			-13	-10	-	dBm	
IP3 _i	input third-order intercept point		[3]	-3	3	-	dBm	
			[4]	-3	3	-	dBm	
IMD3	third-order intermodulation	measured at output pin	[3]	-	-47	-	dBm	
	distortion	measured at output pin	[4]	-	-89	-	dBm	
t _{on}	turn-on time	time from $V_{I(\text{ENABLE})}$ ON, to 90 % of the gain		-	-	2	μs	
t _{off}	turn-off time	time from $V_{I(\text{ENABLE})}$ OFF, to 10 % of the gain		-	-	1	μs	

PCB losses are subtracted

[1] [2] Including PCB losses

[3] [4]

 $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_i = -20 \text{ dBm per carrier}$ $f_1 = 1713 \text{ MHz}$, $P_i = -20 \text{ dBm}$; $f_2 = 1850 \text{ MHz}$, $P_i = -65 \text{ dBm}$

Table 9. Characteristics at V_{CC} = 2.85 V $f = 1575 \text{ MHz}; V_{CC} = 2.85 \text{ V}; V_{l(ENABLE)} \ge 0.8 \text{ V}; P_i < -40 \text{ dBm}; T_{amb} = 25 \text{ °C}; input matched to 50 \Omega using a 6.8 nH inductor, see Figure 1; unless otherwise specified.}$

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I _{CC}	supply current	V _{I(ENABLE)} ≥ 0.8 V					
		P _i < -40 dBm		-	4.6	6.6	mA
		P _i = -20 dBm		-	10	-	mA
		V _{I(ENABLE)} ≤ 0.3 V		-	-	1	μA
G _p	power gain	no jammer		16.5	18.5	20.5	dB
		P _{jam} = -20 dBm; f _{jam} = 850 MHz		-	20.0	-	dB
		P _{jam} = -20 dBm; f _{jam} = 1850 MHz		-	20.5	-	dB
RL _{in}	input return loss	P _i < -40 dBm		8	13	-	dB
		P _i = -20 dBm		-	22	-	dB
RL _{out}	output return loss	P _i < -40 dBm		8	13	-	dB
		P _i = -20 dBm		-	12	-	dB
ISL	isolation			27	30	-	dB
NF	noise figure	P _i = -40 dBm, no jammer	[1]	-	0.55	1.1	dB
		P _i = -40 dBm, no jammer	[2]	-	0.60	1.15	dB
		P _{jam} = -20 dBm; f _{jam} = 850 MHz	[2]	-	0.9	-	dB
		P _{jam} = -20 dBm; f _{jam} = 1850 MHz	[2]	-	1.3	-	dB
P _{i(1dB)}	input power at 1 dB gain compression			-11	-7	-	dBm
IP3 _i	input third-order intercept point		[3]	0	6	-	dBm
			[4]	0	6	-	dBm
IMD3	third-order intermodulation	measured at output pin	[3]	-	-53	-	dBm
	distortion	measured at output pin	[4]	-	-96	-	dBm
t _{on}	turn-on time	time from $V_{I(\text{ENABLE})}$ ON, to 90 % of the gain		-	-	2	μs
t _{off}	turn-off time	time from $V_{I(\text{ENABLE})}$ OFF, to 10 % of the gain		-	-	1	μs

PCB losses are subtracted [1]

[2] [3] [4]

Including PCB losses $f_1 = 1713 \text{ MHz}; f_2 = 1851 \text{ MHz}; P_i = -20 \text{ dBm per carrier}$ $f_1 = 1713 \text{ MHz}, P_i = -20 \text{ dBm}; f_2 = 1850 \text{ MHz}, P_i = -65 \text{ dBm}$

9 Application information

9.1 GNSS application



Table 10. List of components

For schematics see Figure 1.

Component	Description	Value	Remarks	
C1	decoupling capacitor	1 nF	to suppress power supply noise	
IC1	BGU8019	-	NXP	
L1	high-quality matching inductor	6.8 nH	Murata LQW15A	



9.2 Graphs

















SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS, Galileo, and Compass



f = 1575 MHz.

- 1. T_{amb} = -40 °C
- 2. T_{amb} = +25 °C
- 3. T_{amb} = +85 °C

Figure 28. Input power at 1 dB gain compression as a function of supply voltage; typical values





 f_1 = 1713 MHz; f_2 = 1851 MHz; Pi = -20 dBm at $f_1;$ Pi = -65 dBm at $f_2;$ T_{amb} = 25 °C.

Figure 29. Input third order intercept point and third order intermodulation distortion as function of supply voltage; typical values



10 Package outline



Figure 32. Package outline SOT1232 (XSON6)

11 Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A* or equivalent standards.

12 Abbreviations

Table 11. Abbreviations				
Acronym	Description			
ESD	electrostatic discharge			
GLONASS	global navigation satellite system			
GNSS	global navigation satellite system			
GPS	global positioning system			
НВМ	human body model			
MMIC	monolithic microwave-integrated circuit			
PCB	printed circuit board			
SiGe:C	silicon germanium carbon			

13 Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
BGU8019 v.5	20210521	Product data sheet	2021050321	BGU8019 v.4.2				
Modifications:	 changed cond 	 added Min, and or Max values to some characteristics at 2.85 V changed conditions, footnotes, and Minimum, and Typical values on IP3_i added extra Typical value, and footnote to IMD3 						
BGU8019 v.4.2	20190516	Product data sheet	-	BGU8019 v.4.1				
Modifications:	 added general 	Marking diagram						
BGU8019 v.4.1	20190510	Product data sheet	-	BGU8019 v.4				
Modifications:	 adapted date date date date date date date d	code notation to the Marking	g code table					
BGU8019 v.4	20181123	Product data sheet	-	BGU8019 v.3				
Modifications:	 adapted in bar 	ent min and max values in th nd, and out of band condition ame of the application into (n to IP3 _i parameter					
BGU8019 v.3	20170118	Product data sheet	-	BGU8019 v.2				
Modifications:	• <u>Section 1</u> : add	ed GPS1202M according to	our new naming conv	rention				
BGU8019 v.2	20140603	Product data sheet	-	BGU8019 v.1				
BGU8019 v.1	20131112	Preliminary data sheet	-	-				

14 Legal information

14.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition	
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.	
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.	
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[2] The term 'short data sheet' is explained in section "Definitions".

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Contents

1	Product profile	1
1.1	General description	1
1.2	Features and benefits	1
1.3	Applications	2
1.4	Quick reference data	
2	Pinning information	
3	Ordering information	
4	Marking code	
5	Limiting values	4
6	Recommended operating conditions	4
7	Thermal characteristics	4
8	Characteristics	5
9	Application information	7
9.1	GNSS application	
9.2	Graphs	7
10	Package outline	15
11	Handling information	16
12	Abbreviations	
13	Revision history	16
14	Legal information	

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