Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS8110T

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat} and corresponding low RCEsat
- High collector current capability
- · High collector current gain
- · Improved efficiency due to reduced heat generation

3. Applications

- Major application segments
 - · Automotive 42 V power
 - · Telecom infrastructure
 - Industrial
- DC/DC converters
- Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load driver (e.g. relays, buzzers and motors)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-100	V
I _C	collector current		-	-	-1	Α
I _{CM}	peak collector current	limited by T _{j(max)}	-	-	-3	Α
R _{CEsat}	collector-emitter saturation resistance	I_C = -1 A; I_B = -100 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	170	320	mΩ



100 V, 1 A PNP low VCEsat transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	_
2	Е	emitter		C
3	С	collector		В—
			1 2	E sym132
			SOT23	

6. Ordering information

Table 3. Ordering information

Table 6. Grading intermation	•					
Type number	Package	ckage				
	Name	Description	Version			
PBSS9110T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS9110T	%U7

[1] % = placeholder for manufacturing site code

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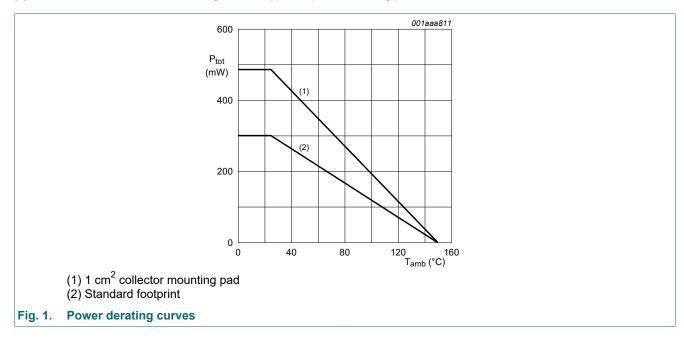
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-120	V
V_{CEO}	collector-emitter voltage	open base		-	-100	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
Ic	collector current			-	-1	Α
I _{CM}	peak collector current	limited by T _{j(max)}		-	-3	Α
I _B	base current			-	-300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



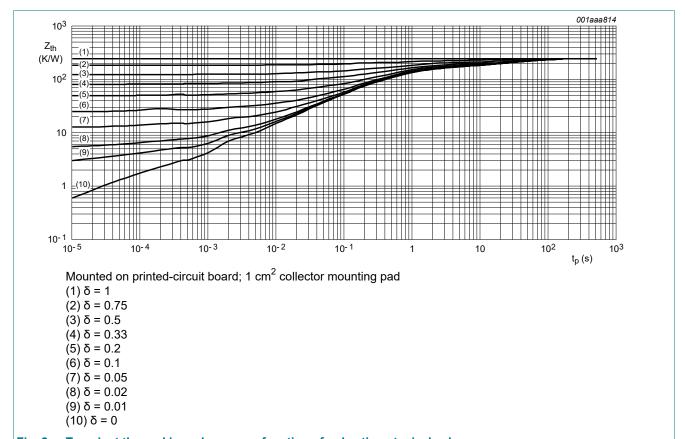
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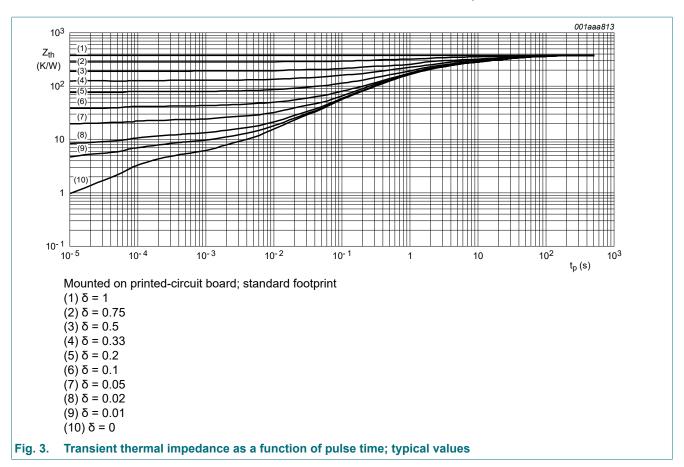
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from	in free air	[1]	-	-	417	K/W
junction to ambient		[2]	-	-	260	K/W	

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



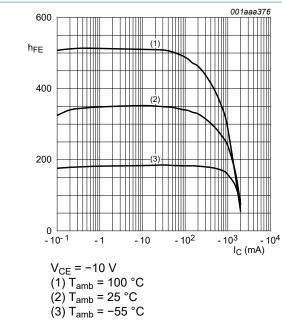


100 V, 1 A PNP low VCEsat transistor

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = -100 \mu A; I_E = 0 A; T_{amb} = 25 °C$	-120	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I_C = -10 mA; I_B = 0 A; T_{amb} = 25 °C	-100	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage (collector open)	I _C = 0 A; T _{amb} = 25 °C	-5	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = -80 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -80 V; I _E = 0 A; T _j = 150 °C	-	-	-50	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -80 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE} DC currer	DC current gain	V _{CE} = -5 V; I _C = -1 mA; T _{amb} = 25 °C	150	-	-	
		V _{CE} = -5 V; I _C = -250 mA; T _{amb} = 25 °C	150	-	-	
		V_{CE} = -5 V; I_{C} = -500 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	150	-	450	
		V_{CE} = -5 V; I_{C} = -1 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	125	-	-	
V _{CEsat}	collector-emitter	I _C = -250 mA; I _B = -25 mA; T _{amb} = 25 °C	-	-	-120	mV
	saturation voltage	I _C = -500 mA; I _B = -50 mA; T _{amb} = 25 °C	-	-	-180	mV
		I_C = -1 A; I_B = -100 mA; pulsed; $t_p \le$	-	-	-320	mV
R _{CEsat}	collector-emitter saturation resistance	300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	170	320	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = -1 A; I _B = -100 mA; T _{amb} = 25 °C	-	-	-1.1	V
V_{BEon}	base-emitter turn-on voltage	V _{CE} = -5 V; I _C = -1 A; T _{amb} = 25 °C	-	-	-1	V
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -50 mA; f = 100 MHz; T_{amb} = 25 °C	100	-	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	17	pF

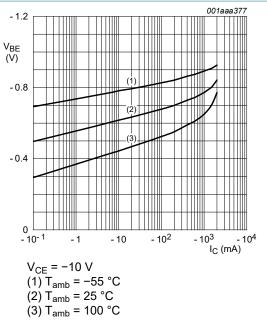


$$(1) T_{amb} = 100 ° ($$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

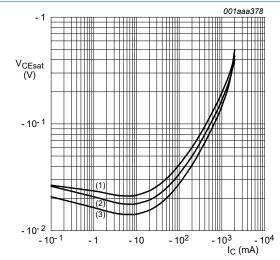
Fig. 4. DC current gain as a function of collector current; typical values



(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



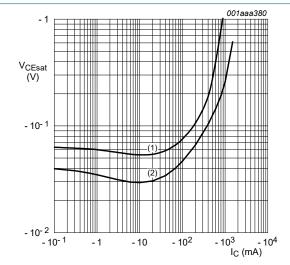
$$I_{\rm C}/I_{\rm B}=10$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values



$$(1) I_{\rm C}/I_{\rm B} = 50$$

(2)
$$I_C/I_B = 20$$

Collector-emitter saturation voltage as a Fig. 7. function of collector current; typical values

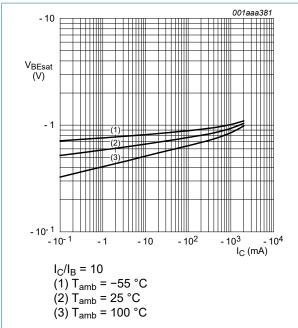


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

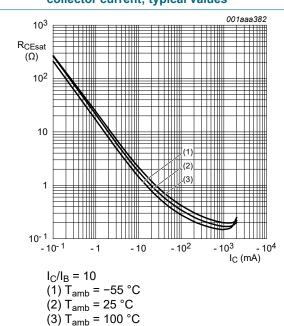


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

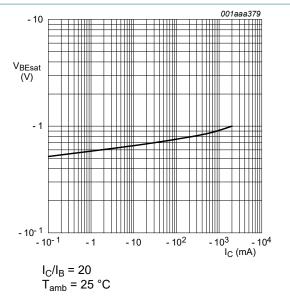


Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values

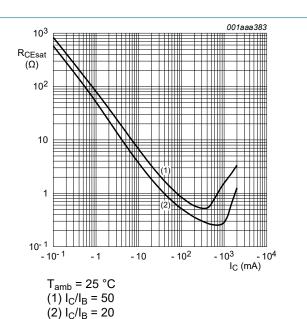
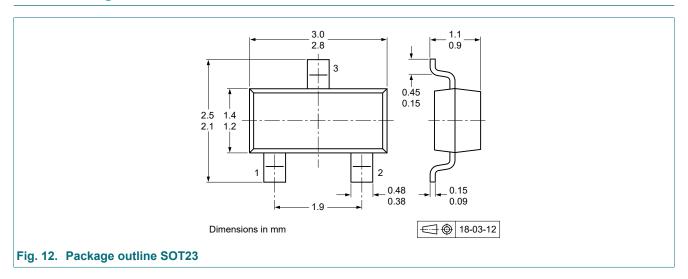


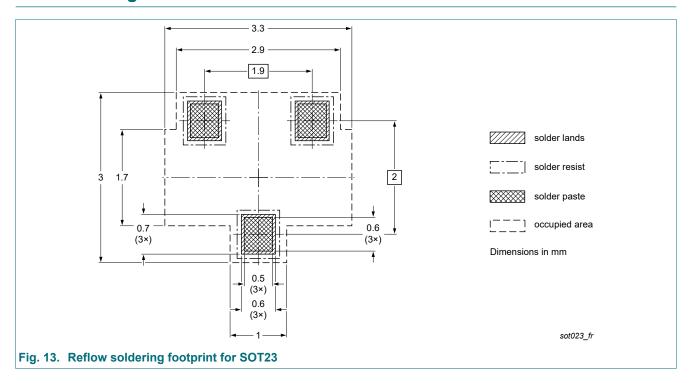
Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

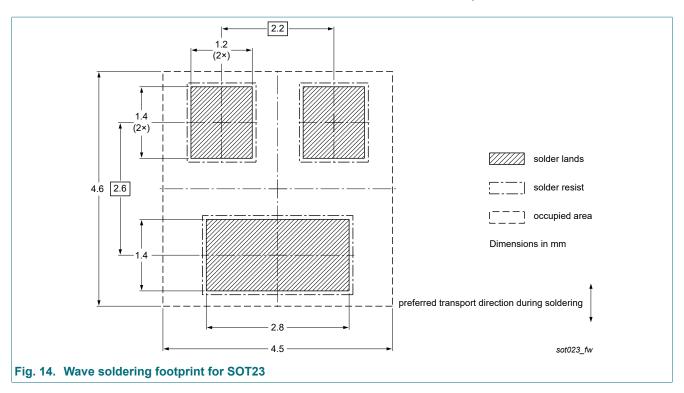
100 V, 1 A PNP low VCEsat transistor

11. Package outline



12. Soldering





100 V, 1 A PNP low VCEsat transistor

13. Revision history

Table 8. Revision history

Table 6. Kevision mistory				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS9110T v.4	20230101	Product data sheet	-	PBSS9110T v.3
Modifications:		anged to non-automotive (-Q) product alternative	•	on. Please refer to nexperia.com for
PBSS9110T v.3	20220523	Product data sheet	-	PBSS9110T v.2
PBSS9110T v.2	20040513	Product data sheet	-	PBSS9110T v.1
PBSS9110T v.1	20040506	Product data sheet	-	-

100 V, 1 A PNP low VCEsat transistor

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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100 V, 1 A PNP low VCEsat transistor

Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Thermal characteristics	4
10.	. Characteristics	6
11.	Package outline	9
12.	. Soldering	9
13.	. Revision history	11
14.	. Legal information	12

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