ON Semiconductor



Title of Change:		NCV7428 Datasheet Update – Revision 3					
Effective date:		27 August 2015					
Contact information:		Contact your local ON Semiconductor Sales Office or Roman Buzas <roman.buzas@onsemi.com></roman.buzas@onsemi.com>					
Type of notification:		ON Semiconductor will consider this change accepted.					
Change category:		□ Wafer Fab Change □ Assembly Change □ Test Change ☑ Other					
Change Sub-Category(s): Manufacturing Site Change/Addition Materia Manufacturing Process Change Product			Change Datasheet/Product Doc change Change Shipping/Packaging/Marking pecific change Other:				
Sites Affected:		pplicable 🗌 ON Semico	onductor site(s) :	External Foundry/Subcon site(s)			
Description and Purpose:							
 Electrical DC Characteristics updated: Specification for VOUT regulator output voltage during exposure to RF electro-magnetic disturbances with no bus capacitance added. Table 6. DC CHARACTERISTICS 							
Symbol	Parameter		Conditions	Min	Тур	Max	Unit
V _{OUT} REGULATOR							
Symbol		Parameter	Conditions	Min	Тур	Max	Unit
V _{OUT_5_EMC}	VOUT regul under EMC	ator output voltage (Note 6)	DPI EMC test applied to LIN pin. No bus capacitor. SOIC8 package; (Note 5)	4.85	5	5.15	V
V _{OUT_33_EMC}	VOUT regul under EMC	ator output voltage (Note 6)	DPI EMC test applied to LIN pin. No bus capacitor. SOIC8 package; (Note 5)	3.201	3.3	3.399	V
 4. In case LIN bus capacitor of at least 82 pF is not used VOUT_5_EMC and VOUT_33_EMC needs to be taken into account. 5. Tested according to: LIN Conformance Test Specification Package for LIN 2.1, October 10th, 2008. Verified by external test house. 6. Values based on design and characterization. Not tested in production. 							
List of Affected S	tandard Par	ts:					
NCV7428D15 NCV7428D13 NCV7428D1L NCV7428D1L NCV7428MW NCV7428MW NCV7428MW NCV7428MW	R2G 5R2G 3R2G /5R2G /3R2G /15R2G						