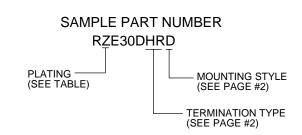
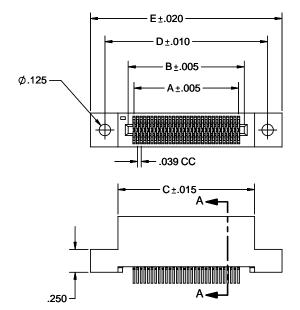
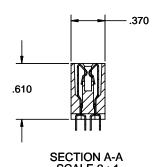
PLATING SPECIFICATIONS							
	CONTACT		TERMINAL				
Α	.000015" GOLD		.0001" TIN/LEAD				
D	.00003" GOLD		.00003" GOLD OVERALL				
G	.00001" GOLD		.000005" GOLD				
М	.00003" GOLD		.00001" GOLD OVERALL				
N	.00005" GOLD		.0001" TIN/LEAD				
0	.00003" GOLD		.0001" TIN/LEAD				
Р	GOLD FLASH		GOLD FLASH				
R	.00002" GOLD		.0001" TIN/LEAD				
S	.00001" GOLD		.00001" GOLD OVERALL				
Т	.0001" TIN/LEAD		.0001" TIN/LEAD OVERALL				
V	.00003" GOLD		.000005" GOLD				
W	GOLD FLASH		.0001" TIN/LEAD				
Х	.00003" GOLD		.0001" TIN/LEAD				
Y	.00003" GOLD		.000005" GOLD				
Z	.00001" GOLD		.0001" TIN/LEAD				



	Α	В	С	D	Е	POSITIONS				
R_E05DH	0.157	0.283	0.507	0.793	1.107	5				
R_E10DH	0.354	0.480	0.704	0.990	1.304	10				
R_E15DH	0.551	0.677	0.901	1.187	1.501	15				
R_E20DH	0.748	0.874	1.098	1.384	1.698	20				
R_E25DH	0.945	1.071	1.295	1.581	1.895	25				
R_E30DH	1.142	1.268	1.492	1.778	2.092	30				
R_E32DH	1.220	1.346	1.570	1.856	2.170	32				
R_E35DH	1.465	1.465	1.689	1.975	2.289	35				
R_E40DH	1.535	1.661	1.885	2.171	2.485	40				
R_E45DH	1.732	1.858	2.082	2.368	2.682	45				

30 POSITION BODY USED AS EXAMPLE ONLY.





SCALE 2:1

NOTES:

- 1. THIS SPECIFICATION COVERS THE REQUIREMENT FOR DUAL EDGECARD CONNECTORS.
- 2. INSULATOR MATERIAL: POLYPHENYLENE SULFIDE.
- 3. CONTACT MATERIAL: PHOSPHOR BRONZE.
 4. PLATING: SEE PLATING SPEC. TABLE AND SAMPLE PART NUMBER
- 5. TEMPERATURE: -65 °C TO 125 °C
- 6. UL FLAMMABILITY RATING: 94V-0 7. VOLTAGE RATING: 125 VDC MINIMUM AT SEA LEVEL.
- 8. CURRENT RATING: 1 AMP

- 9. VOLTAGE DROP: 30 MILLI VOLT AT RATED CURRENT
 10. INSULATION RESISTANCE: 1000 MEGA OHM
 11. CONNECTOR IDENTIFICATION: THE PART SHALL BE MARKED WITH A PART NUMBER AND DATE CODE.

- 12. BOARD THICKNESS ACCOMMMODATED: .054/.070
 13. BOARD INSERTION FORCE: 6 OUNCE AVERAGE PER CONTACT PAIR USING .062 PCB
 14. BOARD WITHDRAWAL FORCE: 1 OUNCE MINIMUM PER CONTACT PAIR USING .062 PCB
- 15. TOLERANCE: ±.010" UNLESS OTHERWISE NOTED.

	Α	101	ORIGINAL RELEASE				11/24/03	JFW	AJT	
COMPUTER	REV.	ECO NO.	DESCRIPTION				DATE	BY	APPV'D	
GENERATED DOCUMENT DO			UNLESS OTHERWISE SPECIFIED:		DATE	NAME				_
NOT CHANGE			DIMENSIONS ARE IN INCHES TOLERANCES:	DRAWN	11/25/03	JWHITE	SULLINS			5
MANUALLY			FRACTIONAL ± 1/32 ANGULAR: ± 1	CHECKED	11/25/03	AJT	1 6	ELECT	RONICS	
			TWO PLACE DECIMAL ± .02 THREE PLACE DECIMAL ± .005	COMMENTS: P.			PART NO/DESCRIPTION: HH,HF,HR TERMINATIONS			
PROPRIETARY AND CONFIDENTIAL			FOUR PLACE DECIMAL ± .0005							
THIS DRAWING AND DESIGN IS THE PROPERTY OF SULLINS ELECTRONICS.			INTERPRET GEOMETRIC TOLERANCING PER:				'''	R_E_	DH	
IT MAY NOT BE USED FOR ANY PURPOSE OTHER THAN BY THE OWNER			MATERIAL NOTED	4			SIZE	DWG. NO.		REV
SULLINS ELECTRONICS DOES NOT AUTHORIZE THE REPRODUCTION OR CONVEYANCE OF ANY INFORMATION	NEXT ASSY	USED ON	FINISH N/A	\bigoplus		_	С	10	0092	Α
CONTAINED HEREIN.	AF	PLICATION	DO NOT SCALE DRAWING				SCAL	E: 4:1		SHEET 1 OF 2

