Digital Attenuator, Constant Phase 15.5 dB, 5-Bit, TTL Driver, DC - 4 GHz

Features

- Attenuation: 0.5 dB Steps to 15.5 dB
- Minimal Phase Variation over Attenuation Range
- Low DC Power Consumption
- Small Footprint, PQFN Package
- Integral TTL Driver
- 50 Ω Impedance
- Test Boards are Available
- RoHS* Compliant

Description

The MAAD-009170 is a GaAs pHEMT 5-bit digital attenuator with integral TTL driver in an PQFN plastic surface mount package. Step size is 0.5 dB providing a 15.5 dB total attenuation range. This design has been optimized to minimize phase variation over the attenuation range.

MAAD-009170 is ideally suited for use where accuracy, fast switching, very low power consumption and low intermodulation products are required. Typical applications include dynamic range setting in precision receiver circuits and other gain/ leveling control circuits.

Functional Schematic



Pin Configuration²

Pin #	Function			
1, 7, 11, 13, 26, 28	GND			
2	C5			
3	C4			
4	C3			
5	C2			
6	C1			
8, 9, 14 - 25, 31	NC			
10, 29	NC ³			
12	RF1			
27	RF2			
30	Vee			
32	+Vcc			

1. Reference Application Note M513 for reel size information.

The exposed pad centered on the package bottom must be connected to RF and DC ground.

3. Pins 10 & 29 must be isolated.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

Package

Bulk Packaging

1000 piece reel

Sample Test Board

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Ordering Information¹

Part Number

MAAD-009170-000100

MAAD-009170-0001TR

MAAD-009170-0001TB

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Electrical Specifications: $T_A = 25^{\circ}C$, $Z_0 = 50 \Omega$, $V_{CC} = +5 V$, $V_{EE} = -5 V$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Operating Power ⁴	_	dBm			20
Reference Insertion Loss	DC - 2.0 GHz 2.0 - 4.0 GHz	dB			4.7 5.2
Attenuation ⁵	Above Loss, DC - 4.0 GHz 0.5 dB 1.0 dB 2.0 dB 4.0 dB 8.0 dB 15.5 dB	dB	0.24 0.73 1.71 3.67 7.59 14.94	_	0.76 1.27 2.29 4.33 8.41 16.20
Phase Accuracy Relative to Reference Loss State	Any Single Bit DC - 2.0 GHz 2.0 - 4.0 GHz Any Combination of Bits DC - 2.0 GHz 2.0 - 4.0 GHz	o	_	_	±2 ±3 ±4 ±7
VSWR	Full Range DC - 4.0 GHz	Ratio			1.9:1
Switching Speed T _{ON} T _{OFF} T _{RISE} T _{FALL}	1.3 V Cntl to 90% RF 1.3 V Cntl to 10% RF 10% RF to 90% RF 90% RF to 10% RF	ns		47 24 23 13	_
1 dB Compression ⁶	Reference State 0.05 GHz 0.5 - 4.0 GHz	dBm	_	+26 +26	_
Input IP3	Two-tone inputs up to +5 dBm 0.05 GHz 0.5 - 4.0 GHz	dBm		+43 +40	_
Input IP2	Two-tone inputs up to +5 dBm 0.05 GHz 0.5 - 4.0 GHz	dBm		+50 +72	
V _{CC} V _{EE}	_	V	4.5 -8.0	5.0 -5.0	5.5 -4.5
V _{IL} V _{IH}	LOW-level input voltage HIGH-level input voltage	V	0.0 2.0	0.0 5.0	0.8 5.0
I _{IN} (Input Leakage Current)	$V_{IN} = V_{CC}$ or GND	μA	-1.0		1.0
I _{CC} (Quiescent Supply Current)	Vcntrl = V _{CC} or GND	μA	_	250	400
ΔI _{cc} (Additional Supply Current Per TTL Input Pin)	V _{CC} = Max. Vcntrl = V _{CC} - 2.1 V	mA	_	_	1.0
IEE	V_{EE} min. to max, V_{IN} = V_{IL} or V_{IH}	mA	-1.0	-0.2	—
Thermal Resistance (θ _{JC})	Thermal Resistance (θ_{JC}) —		_	35	_

4. Maximum operating power is specified with the input applied to RF1. If the input is applied to RF2, then maximum operating power is 16 dBm.

5. This attenuator is guaranteed monotonic.

6. 1 dB Compression was measured up to 26 dBm, which is the absolute maximum rating for this device.

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Absolute Maximum Ratings^{7,8}

Parameter	Absolute Maximum			
Input Power ⁹ DC - 4.0 GHz	+26 dBm			
V _{CC}	$-0.5 \text{ V} \le \text{V}_{\text{CC}} \le +7.0 \text{ V}$			
V _{EE}	$-8.5~\textrm{V} \leq \textrm{V}_{\textrm{EE}} \leq +0.5~\textrm{V}$			
V _{CC} - V _{EE}	-0.5 V \leq V _{CC} - V _{EE} \leq 14.5 V			
V _{IN} ¹⁰	$-0.5 \text{ V} \leq \text{Vin} \leq \text{V}_{\text{CC}} + 0.5 \text{ V}$			
Operating Temperature	-40°C to +85°C			
Storage Temperature	-65°C to +125°C			

7. Exceeding any one or combination of these limits may cause permanent damage to this device.

- MACOM does not recommend sustained operation near these survivability limits.
- 9. The maximum operating power is specified with the input applied to RF1. If the input is applied to RF2, then maximum operating power is +22 dBm
- 10.Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Recommended PCB Configuration¹¹



11. Application Note S2083 is available on the MACOM website.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Moisture Sensitivity

The MSL rating for this part is defined as Level 2 per IPC/JEDEC J-STD-020. Parts shall be stored and/or baked as required for MSL Level 2 parts.

Truth Table (Digital Attenuator)¹²

C5	C4	C3	C2	C1	Attenuation	
0	0	0	0	0	Loss, Reference	
0	0	0	0	1	0.5 dB	
0	0	0	1	0	1.0 dB	
0	0	1	0	0	2.0 dB	
0	1	0	0	0	4.0 dB	
1	0	0	0	0	8.0 dB	
1	1	1	1	1	15.5 dB	

12. 0 = TTL Low; 1 = TTL High

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Typical Performance Curves

Reference Loss vs. Frequency



Attenuation - 1 dB Bit vs. Frequency



Attenuation - 4 dB Bit vs. Frequency



Attenuation - 0.5 dB Bit vs. Frequency



Attenuation - 2 dB Bit vs. Frequency



Attenuation - 8 dB Bit vs. Frequency



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Typical Performance Curves

Attenuation - 15.5 dB Attenuation vs. Frequency



Phase - 1 dB Bit vs. Frequency Relative to Reference Loss State



Phase - 4 dB Bit vs. Frequency Relative to Reference Loss State



Phase - 0.5 dB Bit vs. Frequency Relative to Reference Loss State



Phase - 2 dB Bit vs. Frequency Relative to Reference Loss State



Phase - 8 dB Bit vs. Frequency Relative to Reference Loss State



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Typical Performance Curves





VSWR - 0.5 dB Bit vs. Frequency



VSWR - 2 dB Bit vs. Frequency



VSWR - Reference State vs. Frequency



VSWR - 1 dB Bit vs. Frequency



VSWR - 4 dB Bit vs. Frequency



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Typical Performance Curves

VSWR - 8 dB Bit vs. Frequency



VSWR - 15.5 dB Attenuation vs. Frequency



Typical Input IP2 and IP3 at Room Temperature¹³

Attenuation	IP2			IP3			l lucito
	50 MHz	500 MHz	2 GHz	50 MHz	500 MHz	2 GHz	Units
Reference State	50	72	73	43	40	44	dBm
0.5 dB	51	73	74	43	41	44	dBm
1 dB	51	73	75	43	41	44	dBm
2 dB	51	73	74	43	41	45	dBm
4 dB	51	73	74	43	41	45	dBm
8 dB	50	71	75	41	43	41	dBm
15.5 dB	53	74	79	43	42	44	dBm

13. IP2 and IP3 are measured with two-tone inputs F1 and F2 up to +5 dBm with 1 MHz spacing.

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CSP-1, 4 x 6 mm, 32-lead PQFN[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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