

DATA SHEET

CURRENT SENSOR-LOW TCR PR2010

> 5%,1% RoHS Compliant



Product specification – Feb. 22, 2010 V1





Chip Resistor Surface Mount PR SERIES 2010 (RoHS Compliant)

SCOPE

This specification describes PR2010 series current sensor - low TCR chip resistors with lead-free terminations made by metal substrate.

FEATURES

- Products with lead free terminations meet RoHS requirements.
- High component and equipment reliability
- Low thermal EMF(<1uV/℃).
- Ultra-low resistance and narrow tolerance can suitable for current detection.
- Low inductance 0.5nH to 5nH.

Product Applications

- Battery Pack,
- Inverter/ Converter (DC-DC/AC-DC/DC-AC)
- Consumer Electrics
- Laptop

ORDERING INFORMATION

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient of resistance, taping reel, resistance value and special packing quantity.

$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	MARKING PR2010	
(1) TOLERANCE		
J = ±5% F = ±1%	R004	
(2) PACKAGING TYPE	Fig.1 Value=4mΩ	
K = Embossed taping reel	(4 digits, resistance greater or equal than $4m\Omega$)	
(3) TEMPERATURE COEFFICIENT OF RESISTANCE E=±50ppm/°C	R001	
(4) TAPING REEL	- Fig.2 Value=1mΩ	
07 = 7" dia. Reel & 0.5W 7W = 7" dia. Reel & 1W	(4 digits, resistance below or equal than $3m\Omega$)	
(5) RESISTANCE VALUE	ORDERING EXAMPLE	
PR: 0R001~ 0R1 (1mΩ~100mΩ)	The ordering code for a PR2010 0.5W chip resistor, va 0.015Ω with ±1% tolerance, supplied in 7-inch tape re with 2Kpcs quantify is:	
(6) Special Packaging Quantity	PR2010FKE070R015Z.	
Z = 2,000 units/reel		
	www.yageo.com	

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DIMENSION

_⊢ Table 1		
PR2010	1 m Ω~3 m Ω	4 mΩ~100 mΩ
L (mm)	5.10±0.25	5.10±0.25
W (mm)	2.54±0.25	2.54±0.25
H (mm)	0.80±0.25	0.64±0.25
l1 (mm)	1.30±0.25	0.80±0.25

CONSTRUCTION

The resistors are constructed in high grade materials. Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of metal alloy. See fig. 3.

Fig.3 Fig.3 Fig.3 Fig.2 Fig.2

Fig.4 Chip resistor outlines

POWER RATING

PR2010 rated power at 70°C is 0.5W & 1W



Maximum dissipation (P) in percentage of rated power as a function of the operating ambient temperature $({\rm T}_{\rm amb})$

RATED VOLTAGE:

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$U = \sqrt{(P * R)}$$

Where

U=Continuous rated DC

or AC (rms) working voltage

P=Rated power

R=Resistance value





For dimension see Table 1

Table 2	
CHARACTERISTICS	PR2010 0.5W & 1W
Operating Temperature Range	–55°C to +155°C
Maximum Working Voltage	$\sqrt{(P*R)}$
Resistance Range	1mΩ~100mΩ
Temperature Coefficient	±50ppm/ ℃



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TAPING REEL

____Table 3

DIMENSION	2010
Tape Width(mm)	8
ØA (mm)	178.0±1.0
ØN (mm)	60.0±0.5
ØC (mm)	13.50±0.5
ØD (mm)	17.70±0.5
W1 (mm)	13.0±0.5
W2 (mm)	16.2±0.5





EMBOSSED TAPE SPECIFICATION



PACKING STYLE AND PACKAGING QUATITY

PACKING STYLE	REEL	DIMENSION	2010
Embossed Taping Reel	7" (*	178 mm)	2,000 Units



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TESTS AND REQUIREMENTS

TEST	TEST METHOD	PROCEDURE	REQUIREMENT
T.C.R	IEC 60115-1 4.8	At +25/+125 °C Formula: R2-R1 T.C.R= ×10 ⁶ (ppm/°C) R1(t2-t1) Where t1=+25 °C or specified room temperature t2=+125 °C test temperature R1=resistance at room temperature in ohms R2=resistance at test temperature in ohms	Refer to table 2
Life/Endurance	IEC 60115-1 4.25.1	1,000 hours at 70±5 °C applied RCWV 1.5 hours on, 0.5 hour off, still air required	±(1%+0.0005 Ω)
High Temperature Exposure/ Endurance at upper category temperature	IEC 60068-2-2	1,000 hours at maximum operating temperature depending on specification, un-powered No direct impingement of forced air to the parts Tolerances: 155±3 °C	±(1%+0.0005Ω)
Moisture Resistance	MIL-STD-202 Method 106G	Mil-STD-202, Method 106,0% power,7a and 7b not required, t=24h/cycle,10 cycles, Unpowered.	±(0.5%+0.0005Ω)
Short time overload	IEC 60115-1 4.13	4 times RCWV, rating power 5 secs	±(0.5%+0.0005Ω)
Board Flex/ Bending	IEC 60068-2-21	Device mounted on PCB test board as described, only 1 board bending required 2 mm bending Bending time: 60±1 seconds Ohmic value checked during bending	±(1%+0.0005 Ω)
Solder-ability - Wetting	IPC/JEDEC J-STD-002B test B	Electrical Test not required Magnification 50X SMD conditions: 1st step: Method B, aging 4 hours at 155 °C dry heat 2nd step: leadfree solder bath at 245±3 °C Dipping time: 3±0.5 seconds	Well tinned (<u>></u> 95% covered) No visible damage
- Resistance to Soldering Heat	IEC 60068-2-58	Condition B, no pre-heat of samples Leadfree solder, 260±5 °C, 10±1seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(0.5%+0.0005 Ω) No visible damage
Bias Humidity	JIS C5202-7.9	±85 °C,85% RH,10% Bias, Extended Life Test: 1,000 hours, 1.5 hours On, 0.5 hours Off	±(0.5%+0.0005Ω)



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REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 0	2009-03-11		- First issue of this specification
Version 1	2010-02-22		- Marking Rule Defined